北京亿利泰达科技有限公司

Beijing Eletechnic Ltd.

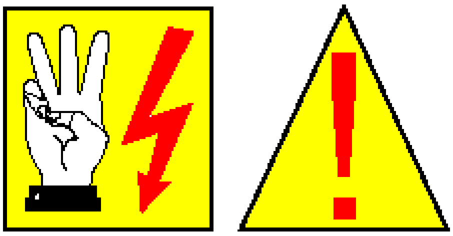
BLMD-4CTV39-1P-SX8 High Voltage Brushless DC Motor Driver

Product Datasheet

BLMD-4CTV39-1P-SX8 is a high voltage, three phase, full wave and hall sensor Brushless DC Motor Driver. Its rated output current is 4ADC. This driver has two sub-types, AC type and DC type. AC type is suitable for 75-275VAC, 50/60Hz, one phase power supply. And DC type is suitable for 75-390VDC power supply.

- Internal AC to DC Rectifier and Filter (Only for AC type)
- Three Phase MOSFETs H-Bridge (15.625kHz PWM)
- Hall Sensor Electrical Phasing 120°/240°
- Reference Voltage for Hall Sensors--+5V
- Motor Over Temperature Protection--PTC
- Forward/Reverse Direction--F/R
- Speed Frequency Generator--FG
- System Malfunction Fault Output--FLT
- Run Enable/Disable--En
- Dynamic Braking--BRK
- PWM Control--ADJ
- PWM Gradient Control--dt
- Motor Current Feedback--CFB
- Input Voltage Feedback--VFB
- Over Current Protection
- Current Limitation
- Over Voltage Lockout
- Under Voltage Lockout
- Heat Sink Over Temperature Protection

BLMD	-	4	C	T	V	39	-	1P	-	S	X8
Brushless Motor Driver		Rated 4ADC	Current Protection	Temperature Protection	Voltage Lockout	Max 390VDC		One Phase Protection		S: Sensored SL: Sensorless	Sub-type



Please read Safety Warning below carefully before installing and operating this driver!

- **This product should be installed and serviced by a qualified technician, electrician, or electrical maintenance person familiar with its operation and the hazards involved.**
- **Proper installation, which includes wiring, mounting in proper enclosure, fusing, cooling, and grounding can reduce the chance of electrical shocks, fires, or explosion in this product or products used with this product, such as motors, coils, hall sensors and/or other circuits connected to it.**
- **Be sure to eliminate body static electricity when operation.**
- **To connect or disconnect J3 or J4 when power on is FORBIDDEN. J3 or J4 phase missing is FORBIDDEN.**
- **Do not touch the PCB board, and/or other circuits connected to it, when power on. Eye protection must be worn and insulated tools must be used when working under power.**
- **All output and input terminals are NOT isolated from the incoming AC mains supply and may be at up to 400V with respect to earth, regardless of the input mains supply voltage applied. These terminals are live during connection. Do not attempt to access these terminals during this time.**

Absolute Maximum Ratings

(The Absolute Maximum Ratings are those values beyond which the safety of the driver cannot be guaranteed)

Parameter	Symbol	Value	Unit
Power Supply Voltage	V_{J1}	275 (rms) 390 (peak)	VAC VDC
Peak Output Current	I_A, I_B, I_C	9.3 (peak)	ADC
Min Permissible Inductance of Motor	L_{Motor}	1	mH
Max Controllable Motor Speed	One Magnetic Pole-pair Rotor	50000	rpm
Hall Reference Voltage Output Current	I_{Hall}	20	mA
Hall, PTC, F/R, EN, BRK Digital Inputs Voltage	V_{Ha}, V_{Hb}, V_{Hc}, V_{PTC}, $V_{F/R}$, V_{EN}, V_{BRK}	-0.5 to +5.5	V
FG, FLT Outputs Current	I_{FG}, I_{FLT}	5 (Source and Sink)	mA
ADJ, dt Analog Inputs Voltage	V_{ADJ}, V_{dt}	-0.5 to +5.5	V
CFB, VFB Outputs Current	I_{CFB}, I_{VFB}	5 (Source and Sink)	mA
Operating Ambient Temperature Range	Ta	-10 to +70	C

Electrical Characteristics

(J1=220VAC, 50Hz, Ta=20C, unless otherwise noted)

Parameter	Symbol	Min	Typical	Max	Unit
J1--Power Supply					
AC 50/60Hz, 1 Phase	V_{AC} I_{AC}	75 -	110/220 5	275 -	VAC AAC
DC	V_{DC} I_{DC}	75 -	155/310 4	390 -	VDC ADC
+5V--Reference Voltage for Hall Sensors					
Output Voltage	V_{Hall}	-	5.2	-	VDC
Output Current	I_{Hall}	-	-	20	mA
Ha, Hb, Hc, PTC--Digital Inputs					
High Threshold Volt	V_{IH}	3.5	-	-	V
Low Threshold Volt	V_{IL}	-	-	1.5	V
High State Current	I_{IH}	-	0	-	mA
Low State Current	I_{IL}	-	5	-	mA
F/R--Digital Input					
High Threshold Volt	V_{IH}	4.5	-	-	V
Low Threshold Volt	V_{IL}	-	-	1.5	V
High State Current	I_{IH}	-	-	1.7	mA
Low State Current	I_{IL}	-0.17	-	-	mA

FG, FLT--Digital Outputs

High State Volt	VOH	3.3	-	-	V
Low State Volt	VOL	-	-	0.9	V
Source Current	IOH	-	-	1	mA
Sink Current	IOL	-1	-	-	mA

EN, BRK--Digital Inputs

High Threshold Volt	VIH	3.5	-	-	V
Low Threshold Volt	VIL	-	-	1.5	V
High State Current	IIH	-	0	-	mA
Low State Current	IIL	-0.25	-	-	mA

ADJ--PWM Control Analog Input

0% / 100% PWM	VADJ	1.28	-	3.83	V
Input Current @3.83V	IIH	-	-	-2.1	uA
Input Current @1.28V	IIL	-6.2	-	-	uA
A/D Resolution	ΔV	-	1/256 (0.01V)	-	-

dt--PWM Gradient Control Analog Input

0us / 355123200us	Vdt	1.28		3.83	V
Input Current @3.83V	IIH	-	-	20	uA
Input Current @1.28V	IIL	-20	-	-	uA
A/D Resolution	ΔV	-	1/256(0.01V)	-	-

CFB--Motor Current Feedback Analog Output

Output Volt Range	VCFB	0	-	6	V
Output Current	ICFB	-	-	1	mA
Coefficient	KCFB	-	1.5625A/1V	-	ADC/VDC

VFB--Input Voltage Feedback Analog Output

Output Volt Range	VVFB	0	-	6	V
Output Current	IVFB	-	-	1	mA
Coefficient	KVFB	-	100V/1V	-	VDC/VDC

Over Current Protection and Current Limitation

Over Current Protection Peak Current	IA, IB, IC	-	9.3	-	ADC
Current Limitation Average Current	IA, IB, IC	-	(Approx.) 7.2	-	ADC

Over Voltage Lockout

Lockout Voltage	VBEMF	400	405	410	VDC
Unlock Voltage	VBEMF	390	395	400	VDC

Under Voltage Lockout

Lockout Voltage	VBEMF	48	50	52	VDC
Unlock Voltage	VBEMF	43	45	47	VDC

Heat Sink Over Temperature Protection

Lockout Temperature	Ts	80	85	90	C
Unlock Temperature	Ts	70	75	80	C

Junction Table

Junction	Pin	Type	Function
J1	L(+)	Power Supply	AC Live Line, DC Positive
	N(-)	Power Supply	AC Null line, DC Negative
	To Earth	-	Earth Line
J2	F/R	Digital Input	Forward/Reverse Direction, TTL Compatible
	FG	Digital Output	Speed Frequency Generator, TTL Compatible
	FLT	Digital Output	System Malfunction Output, TTL Compatible
	EN	Digital Input	Run Enable/Disenable, TTL Compatible
	BRK	Digital Input	Dynamic Braking, TTL Compatible
	DN	Voltage Divider	Potentiometer Down Pin
	ADJ	Analog Input	PWM Control (Open Loop Stepless Speed Control)
	UP	Voltage Divider	Potentiometer Up Pin
	dt	Analog Input	PWM Gradient Control
	GND	Signals GND	Signals GND
	CFB	Analog Output	Motor Current Feedback
	VFB	Analog Output	Input Voltage Feedback
J3	A	Driver Output	A Phase Winding Driver
	B	Driver Output	B Phase Winding Driver
	C	Driver Output	C Phase Winding Driver
J4	GND	Signals GND	Hall Sensors GND
	Ha	Digital Input	A Hall Sensor, TTL Compatible
	Hb	Digital Input	B Hall Sensor, TTL Compatible
	Hc	Digital Input	C Hall Sensor, TTL Compatible
	+5V	Reference Output	Reference Voltage for Hall Sensors
J5	PTC	Digital Input	A Pin of PTC
	GND	Signals GND	Another Pin of PTC

Main Functions Description

J1--Power Supply:

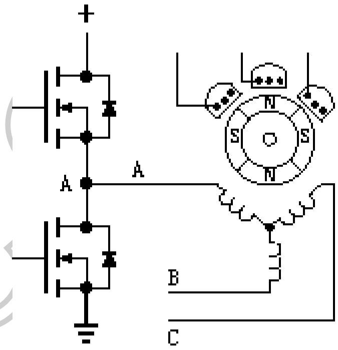
This driver has two sub-types, AC type and DC type.

AC type is suitable for 75-275VAC, 50/60Hz, 1 Phase power supply. There is a build-in rectifier and filter AC to DC converter. Its maximum load capability is about 5AAC. The FUSE is 6A.

DC type is suitable for 75-390VDC power supply. Its maximum load capability is about 4ADC.

Because of the different structures of GND Lines, these two sub-types cannot be substituted for each other. Otherwise the GND Lines will be broken.

J3--A, B, C Three Phase Winding Driver:



The driver internal circuit is shown in right figure. Three Phase, Full wave, H-Bridge could drive either Y or Delta winding motor. Please see the “Commutation Truth Table” for reference.

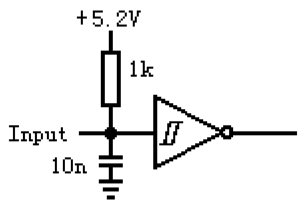
The use of 15.625kHz Pulse Width Modulating (PWM) at the three bottom MOSFETs provides an energy efficient method of controlling the motor speed by varying the average voltage applied to each stator winding during the commutation sequence.

To connect or disconnect J3 when power on is FORBIDDEN! J3 phase missing is FORBIDDEN!

+5V--Reference Voltage for Hall Sensors:

+5.2VDC reference power could output only 20mA for hall sensors. It is FORBIDDEN to supply any other loads!

Ha, Hb, Hc--Hall Digital Inputs:

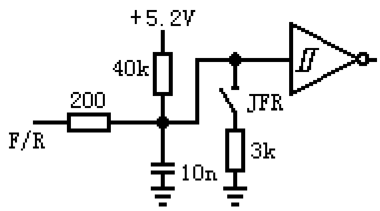


TTL compatible. The internal circuit is shown in right figure. Please see the “Commutation Truth Table” for reference.

The hall sensor electrical phasing must be 120°/240°. And Ha, Hb, Hc signals must be connected correctly according to A, B, C windings. Otherwise the driver and motor may be damaged!

To connect or disconnect J4 when power on is FORBIDDEN! J4 phase missing is FORBIDDEN!

F/R--Forward/Reverse Direction Digital Input:

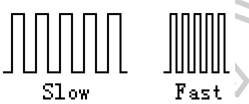


TTL compatible. The internal circuit is shown in right figure. Please see the “Commutation Truth Table” for reference.

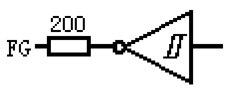
When F/R signal is high or float, the rotation direction of motor is forward. When F/R is low, it is reverse. When F/R is float, JFR switch could change the direction manually.

The running direction also depends on the structure of BLDC motor.

FG--Speed Frequency Generator Digital Output:

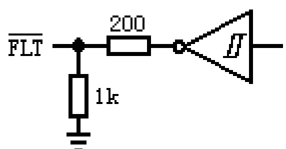


TTL compatible. The internal circuit is shown in right figure. Its frequency is linear to the motor speed. Pulse duty cycle is about 50%. The output waveforms are shown in left figure.



FG (Hz) = Speed (rpm) * N * 3 / 60. N means the number of magnetic pole-pairs (NOT POLES) of the rotor.

FLT--System Malfunction Digital Output:

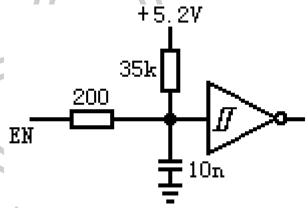


TTL compatible. The internal circuit is shown in right figure. Please see the “Commutation Truth Table” for reference.

A logic high means the motor works normally. A logic low means there are something wrong and causes the FLT Led on and H-Bridge off (Z state).

There is a 1k resistance between FLT pin and GND, to provide logic low when power off.

En--Run Enable/Disenable Digital Input:



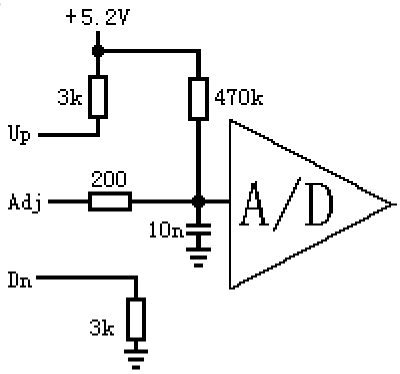
TTL compatible. The internal circuit is shown in right figure. Please see the "Commutation Truth Table" for reference.

A logic high or float causes motor works normally, while a low causes H-Bridge off (Z state) and motor to coast.

BRK--Dynamic Braking Digital Input:

Please see chapter "How to Use BRK Function" for details.

ADJ--PWM Control Analog Input:



Open loop stepless speed control. Analog signal. The internal circuit is shown in right figure.

An A/D converts the voltage of ADJ pin to a byte (0x00 to 0xFF). Software assigns this byte value to PWM. The Pulse Width Modulating at the three bottom MOSFETs controls the motor speed by varying the average voltage applied to each stator winding.

A/D resolution is 1/256 (0.01V). The functional relation is piecewise function. Its expression is:

1. PWM=0x00, PWM%=0%, (U_{ADJ}<1.28);
2. PWM=(U_{ADJ}-1. 28)*100, PWM%=((U_{ADJ}-1.28)*100/255)*100%, (1.28<=U_{ADJ}<=3.83);
3. PWM=0xFF, PWM%=100%, (3.83<U_{ADJ}).

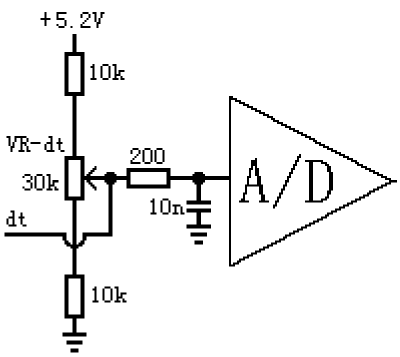
There are three ways to control speed:

First, connect the top foot and bottom foot of a 10kOhm potentiometer to the UP pin and DN pin of J2 separately. And connect the middle foot of the potentiometer to ADJ pin. 10kOhm must be used, other value is not matching.

Second, using an operational amplifier (or D/A). Connect the output of operational amplifier (or D/A) directly to ADJ pin. Leave UP pin and DN pin floating.

Third, connect a filtered Pulse Width Modulation signal to ADJ pin. Leave UP pin and DN pin floating. There must be an external RC filter. RC>640us and f>15kHz are recommended.

dt--PWM Gradient Control Analog Input:



Analog signal. The internal circuit is shown in right figure.

ADJ controls the value of PWM, and dt controls the gradient of PWM. The physical significance of dt is the time constant of PID control.

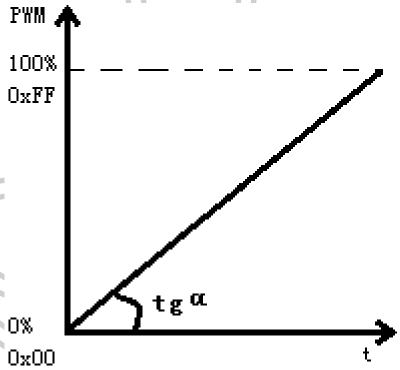
The software uses three steps to convert the analog voltage to a time constant.

First step: An A/D converts the voltage of dt pin to a byte (0x00 to 0xFF). A/D resolution is 1/256 (0.01V). The functional relation is piecewise function, the same as ADJ. Its expression is:

1. n=0x00, (U_{dt}<1.28);
2. n=(U_{dt}-1. 28)*100, (1.28<=U_{dt}<=3.83);
3. n=0xFF, (3.83<U_{dt}).

Second step: In order to extend control range, software does a square, N=n*n. N is a word (two bytes). N could only be 0, 1, 4, 9……65025.

The last step: dt=N*(64/3)us, the unit is us. It is just the time constant.



Let's have some samples. As shown in right figure. tga=dPWM/dt= 1/（N*（64/3））=3/64/N, the unit is 1/us.

1. If n=0, then N=0, then dt=0us, dPWM/dt=tga=infinity. So the software assigns ADJ value to PWM immediately, not any time constant delay.

2. If n=1, then N=1, then dt=1*(64/3)=21.3us. That means, if now ADJ changes, software would not assigns ADJ value to PWM immediately. It would do PWM=PWM+1 (or PWM-1) every 21.3us. So PWM line slopes up or down gradually, tga=dPWM/dt= 3/64 (1/us). Let's suppose: Now ADJ suddenly steps up from 1.28V to 3.83V, then PWM will start from 0x00, and PWM++ every 21.3us. So after 5461us (256 times PWM++), PWM slopes up to 0xFF (PWM=100%).

3. Because of square algorithm, N could not be 2 or 3.

4. If n=2, then N=4, then dt=4*(64/3)= 85.3us. Software would do PWM=PWM+1 (or PWM-1) every 85.3us. Let's suppose: Now ADJ suddenly steps up from 1.28V to 3.83V, then PWM will start from 0x00, and PWM++ every 85.3us. So after 21845us (256 times PWM++), PWM slopes up to 0xFF (PWM=100%).

5. And so on, N=9, 16……65025.

6. The Max time constant: n=255, N=n*n=65025, dt=N*(64/3)=65025*64/3=1387200us. Let's suppose: Now ADJ suddenly steps up from 1.28V to 3.83V, then PWM will start from 0x00, and PWM++ every 1387200us. So after 355123200us=5min55s (256 times PWM++), PWM slopes up to 0xFF (PWM=100%).

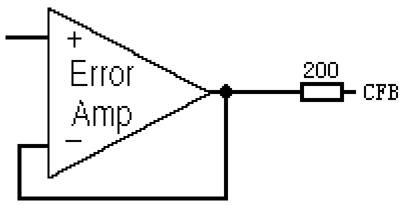
There are three ways to control speed:

First, adjust the VR potentiometer according to the arrow instructions on board "SLOW-FAST".

Second, using an operational amplifier (or D/A). Connect the output of operational amplifier (or D/A) directly to dt pin.

Third, connect a filtered Pulse Width Modulation signal directly to dt pin. There must be an external RC filter. RC>640us and f>15kHz are recommended.

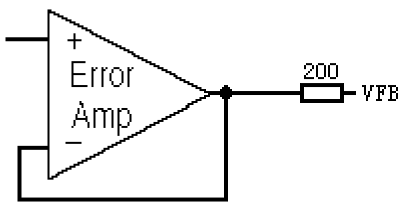
CFB--Motor Current Feedback Analog Output:



Analog signal. The internal circuit is shown in right figure.

This signal feeds back the current of BLDC motor, its unit is DC Ampere. It is linear. Please see the "Electrical Characteristics Table" for its Coefficient.

VFB--Input Voltage Feedback Analog Output:



Analog signal. The internal circuit is shown in right figure.

This signal feeds back the rectified and filtered DC voltage of this driver. Its unit is DC Volt. It is linear. Please see the "Electrical Characteristics Table" for its Coefficient.

Over Current Protection:

An over current protection circuit is inside this driver in order to limit the current of J3 H-Bridge. Please see the "Electrical Characteristics Table" for reference.

When current meets the upper limit, over current protection is active, and FLT pin low, FLT Led on, H-Bridge off (Z state). When current returns to normal, driver auto-restart, and FLT pin high, FLT Led off, H-Bridge on. Please see the “Commutation Truth Table” for reference.

Normally, the frequency of over current protection would be several kHz. And the motor winding will make a noise of the same frequency.

Current Limitation:

A current limitation PID software is inside this driver. When the average current is higher than the setting value, the PID will decrease the PWM duty cycle in order to reduce the average current to protect the H-Bridge. Please see the “Electrical Characteristics Table” for reference.

Current limitation will not trigger FLT signal. If the voltage of dt pin is less than 1.28V (dt=0), then this function is invalid, that means this function only works when dt>0.

Over Voltage Lockout:

An over voltage lockout has been incorporated. Please see the “Electrical Characteristics Table” for reference.

When the rectified and filtered DC voltage goes up >405VDC, FLT pin low, FLT Led on, H-Bridge off (Z state). When voltage goes down <395VDC, driver auto-restart, and FLT pin high, FLT Led off, H-Bridge on. Please see the “Commutation Truth Table” for reference.

Please note: Over Voltage Lockout function does not have the capability to reduce the dangerous voltage itself. So, even if lockout, the supply power voltage maybe still go up, the driver maybe still in danger.

Under Voltage Lockout:

An under voltage lockout has been incorporated to prevent damage to the IC and the H-Bridge. Please see the “Electrical Characteristics Table” for reference.

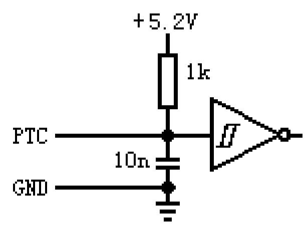
When the rectified and filtered DC voltage goes down <45VDC, FLT pin low, FLT Led on, H-Bridge off (Z state). When voltage goes up >50VDC, driver does not restart immediately. It will wait for 1s, then auto-restart, and FLT pin high, FLT Led off, H-Bridge on. Similarly, when power supply turn on, the driver will wait for 1s first, then start. Please see the “Commutation Truth Table” for reference.

Heat Sink Over Temperature Protection:

A heat sink over temperature protection is inside the driver. Please see the “Electrical Characteristics Table” for reference.

85C sink temperature causes over temperature protection active, and FLT pin low, FLT Led on, H-Bridge off (Z state). When sink temperature drops down below 75C, driver auto-restart, and FLT pin high, FLT Led off, H-Bridge on. Please see the “Commutation Truth Table” for reference.

PTC--Motor Over Temperature Protection Digital Input:



J5 connects to the internal temperature sensor of motor. TTL compatible. The internal circuit is shown in right figure. Please see the “Commutation Truth Table” for reference.

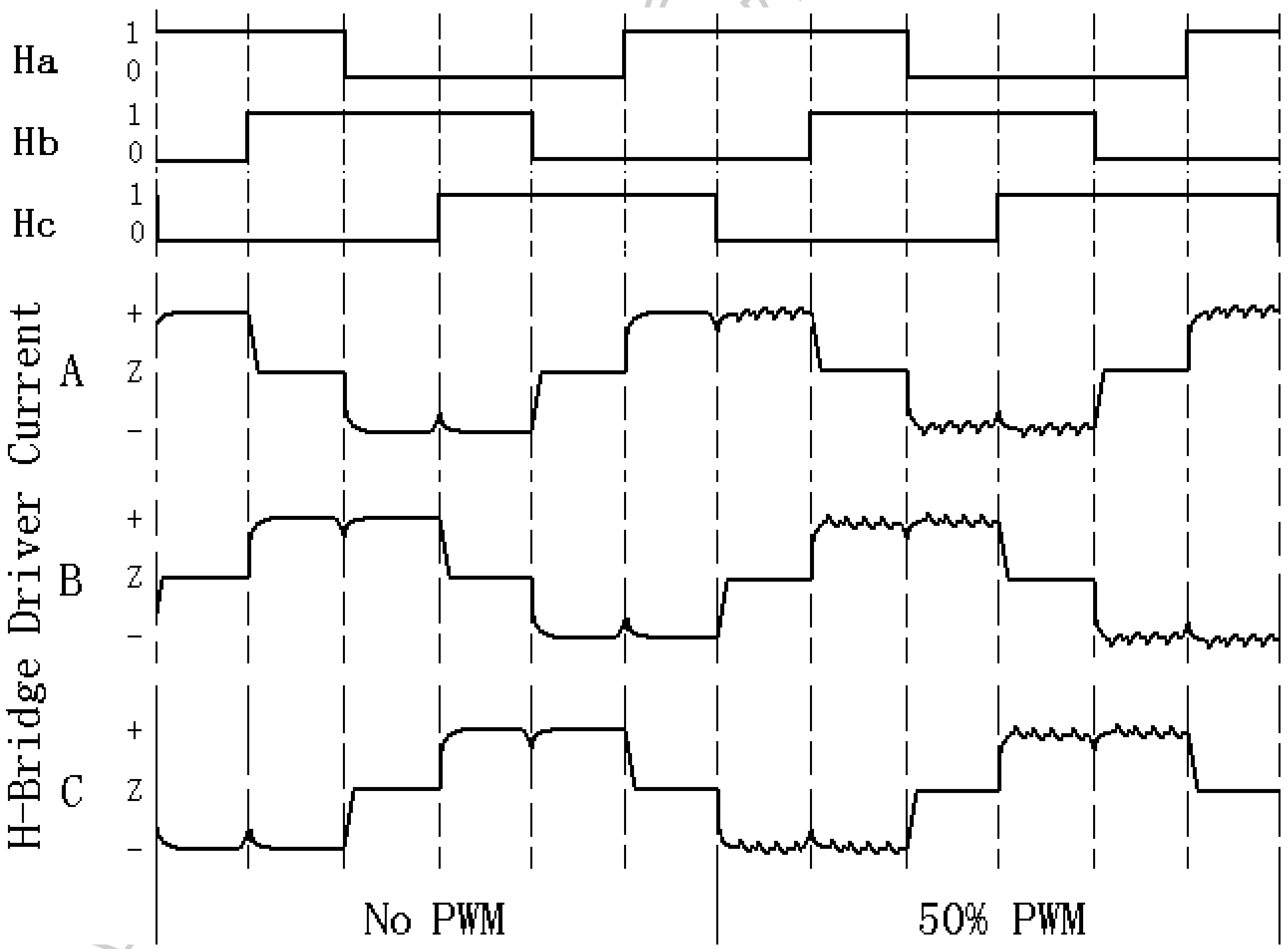
The circuit configuration parameter of J5 is matching for MZ6 series PTC (Positive Temperature Resistance). We could not guarantee J5 match other type of temperature sensor.

If PTC pin is high, FLT pin low, FLT Led on, H-Bridge off (Z state). If PTC pin is low, driver auto-restart, and FLT pin high, FLT Led off, H-Bridge on.

J5 could not be left floating. That means if it is left floating, PTC pin is high, the driver is off (under protection). So, please shortcut PTC to GND if not use.

Commutation Truth Table

Hall Inputs			Control Inputs			PTC OT	OC	OV	UV	Sink OT	H-Bridge Driver			FLT pin	FLT LED
Ha	Hb	Hc	F/R	EN	BRK						A	B	C		
X	X	X	X	X	X	Any One Active					Z	Z	Z	0	ON
1	1	1	X	X	1	All Inactive					Z	Z	Z	0	ON
0	0	0	X	X	1						Z	Z	Z	0	ON
1	1	1	X	X	0						0	0	0	0	ON
0	0	0	X	X	0						0	0	0	0	ON
Six Valid Combinations (Figure Below)			X	0	X						Z	Z	Z	1	OFF
			X	1	0						0	0	0	1	OFF
			1/0	1	1						Normal Commutation (Figure Below)			1	OFF



Normal Commutation Waveforms, F/R=1

Note: "1"=High, "0"=Low, "X"=Don't care, "Z"=High impedance, "+"= Positive current, "-"=Negative current

How to Use BRK Function

Default Setting

The default setting of BRK function is invalid. Please read the instructions below carefully before using this function!

About the Danger and Complexity of Brake Function

The danger and complexity are due to the Over Current Protection, the load and the control circuit.

For the Over Current Protection: When using BRK function, the Over Current Protection and the Current Limitation functions are disabled. Inappropriate operation and control will cause the driver broken!

For the load: Please fix the motor and the load carefully before using this function. Otherwise the load maybe broken by the brake force and people maybe injured!

For the control circuit: There must be an external circuit, accurate control software and a lot of experiments to verify the reliability of all the system, otherwise the driver and/or the motor and/or the external circuit maybe broken!

Simple Theory of Brake Function

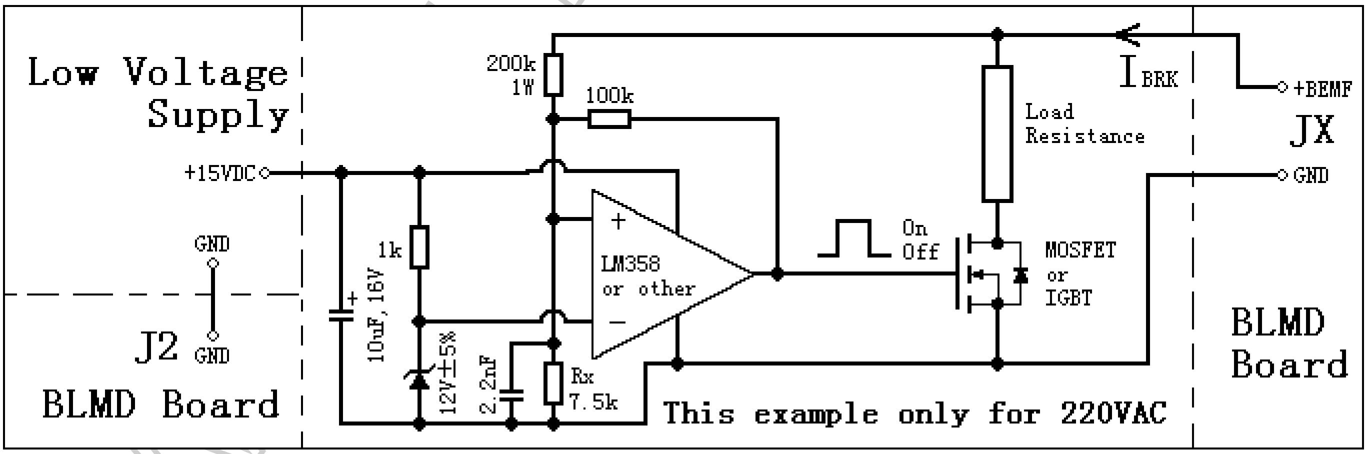
Brushless DC Motor is not only a motor, but also a generator. As well, Brushless DC Motor Driver is not only a driver, but also a rectifier.

Brake function is just using motor as a generator, to convert kinetic energy to three phase electric energy, and then rectify AC to DC, output DC electric energy to JX, and use up or store up electric energy.

There are two keys: First is the external circuit to use up or store up electric energy. Second is control logic.

External Circuit (This example only for reference)

Having an external circuit to use up or store up electric energy is very important. Generator continually converts kinetic energy to electric energy. If electric energy could not be used up, the voltage of JX (BEMF pin) would keep on rising until trigger the Over Voltage Lockout.



Customers must make an external circuit themselves, this circuit is not on our board.

For example, construct an external circuit as shown in above figure, including a comparator, a MOSFET (or IGBT), and a Load Resistance. This circuit could use up the electric energy automatically. When braking, kinetic energy converts to electric energy. The voltage of JX-BEMF pin rises up. The comparator turns on the MOSFET. Load Resistance converts electric energy to thermal energy. BEMF voltage drops down. The comparator turns off the MOSFET.

The power supply of above example is 220VAC. The comparator will turn on the MOSFET at BEMF=360VDC (on level), and turn off at BEMF=340VDC (off level). To increase/decrease the value of Rx will decrease/increase both the on/off level.

Please note: Do not set the off level too low. It must be much higher than 310VDC, because 310VDC is the normal power supply voltage. Likewise, do not set the on level too high, it must be a little lower than 405VDC, because it is the level of Over Voltage Lockout. Please see the "Electrical Characteristics Table" for reference.

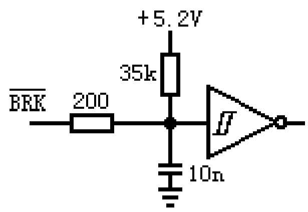
Choosing a proper Load Resistance is very important. Please see the "Absolute Maximum Ratings" for reference. The Rated Output Current is 4ADC. So the min value of Load Resistance is 360VDC/4ADC=90Ohm, otherwise the IBRK will be too high.

Higher value of Load Resistance causes lower IBRK, and longer time duration of braking, and more frequent Over Voltage Lockout, vice versa. The dissipation power of Load Resistance should be chosen according to the power and temperature, normally one or several hundred Watts.

If power supply is 110VAC, please set the comparator on level at BEMF=220VDC, and off level at BEMF=200VDC. Also remember: Do not set the off level too low. It must be much higher than 155VDC, because 155VDC is the normal supply voltage.

Control Logic:

BRK signal is TTL compatible. The internal circuit is shown in right figure. Please see the "Commutation Truth Table" for reference.



A logic high or float allows motor running normally (motor mode), while a low causes motor to brake (generator mode).

In generator mode, ADJ and dt work in the same way as in motor mode. That means braking power (generating capacity) is also determined by PWM on H-Bridge.

Warming again: When using BRK function, the Over Current Protection and the Current Limitation functions are disabled. That means the drive could not limit the braking current automatically. So proper input of ADJ and dt is the only way to control the braking power and current.

In consideration of prudent principle, during the experiment, we suggest: PWM duty cycle (ADJ) from low to high, PWM gradient (dt) from slow to fast, and the value of Load Resistance from high to low.

A sample of braking experiment:

Now let's use nature language to describe a braking software.

Before start, please be sure the external circuit is settled, motor and its loads are fixed properly. The purpose of this experiment is to program a software to brake a running motor.

First, let's suppose the current states of the driver: EN=1 system on, BRK=1 motor running, PWM=100% full speed full power. There are four key points in the following program:

Line1: EN=0;/*Turn off the driver.*/
Line2: dt=0;ADJ=0;
/*This is the first key point: Although now EN=0 and H-Bridge off, PWM is still 0xFF (100%). The changing of EN has no effect on PWM value. If now we let BRK=0, the motor will brake under 100%PWM, it is too hard and dangerous. So we first let dt=0 and ADJ=0. That means we hope PWM=0 as soon as possible.*/
Line3: __delay_cycles(128us+Client Delay);
/*This is the second key point: Why use delay, there are two reasons. First, A/D converter need at least 64us to get the value of dt and ADJ. So delay 128us could guarantee PWM change to 0x00 (0%). Second, client system has time delay too. For example, RC filter, transmission delay and so on. Client must guarantee enough delay time to change the voltage of dt pin and ADJ pin blow 1.28V.*/

Line4: BRK=0;/*Change to generator mode.*/
Line5: EN=1;/*Turn on the driver. Remember dt and ADJ and PWM are all 0x00.*/
Line6: dt=105;/*This is the third key point: dt=105 means the time constant is about 60s (PWM from 0% to 100%). It is only a sample, we are not sure it is appropriate to all application. We only want to express the prudent principle: from low to high, from slow to fast. For first time experiment, dt value should be assigned enough slow than needed.*/
Line7: __delay_cycles(128us+Client Delay);/*This is the fourth key point: We emphasize, here change dt first and delay to guarantee. Then change ADJ. If the contrary, the result is not correct.*/
Line8: ADJ=205;/*The experiment target is change PWM from 0% to 80%. For first time experiment, we suggest not to increase to PWM=100%.*/
……
/*Now PWM is increasing gradually. And motor is decelerating.*/
……
/*Now motor stops.*/
Line9: EN=0; /*Round-off work.*/
Line10: dt=0;ADJ=0;
Line11: __delay_cycles(128us+Client Delay);

For first time experiment, deceleration time maybe too long. Let's improve it gradually.

The purpose of following improvements is to increase the deceleration, reduces the time. We could change two items: First is dt, second is Load Resistance. Please note: Change these two items GRADUALLY.

Step1: Decrease dt to increase the braking power.

Step2: The consequence of Step1 is that Over Voltage Lockout becomes more and more frequently. So to decrease the value of Load Resistance, in order to increase the braking current.

Step3: The consequence of Step2 is that Load Resistance becomes hotter and hotter. So to increase the dissipation power of Load Resistance.

Please repeat above improvements until achieve your purpose.

Please note: The braking power is limited. For the above sample, the max voltage is 360VDC, the rate current is 4ADC, and the min Load Resistance is 90Ohn. So the max braking power is 360*4=360*360/90=1440W. That means, the deceleration could not be infinite, and also it is impossible to reduce the braking time to 0.

The Final Step:

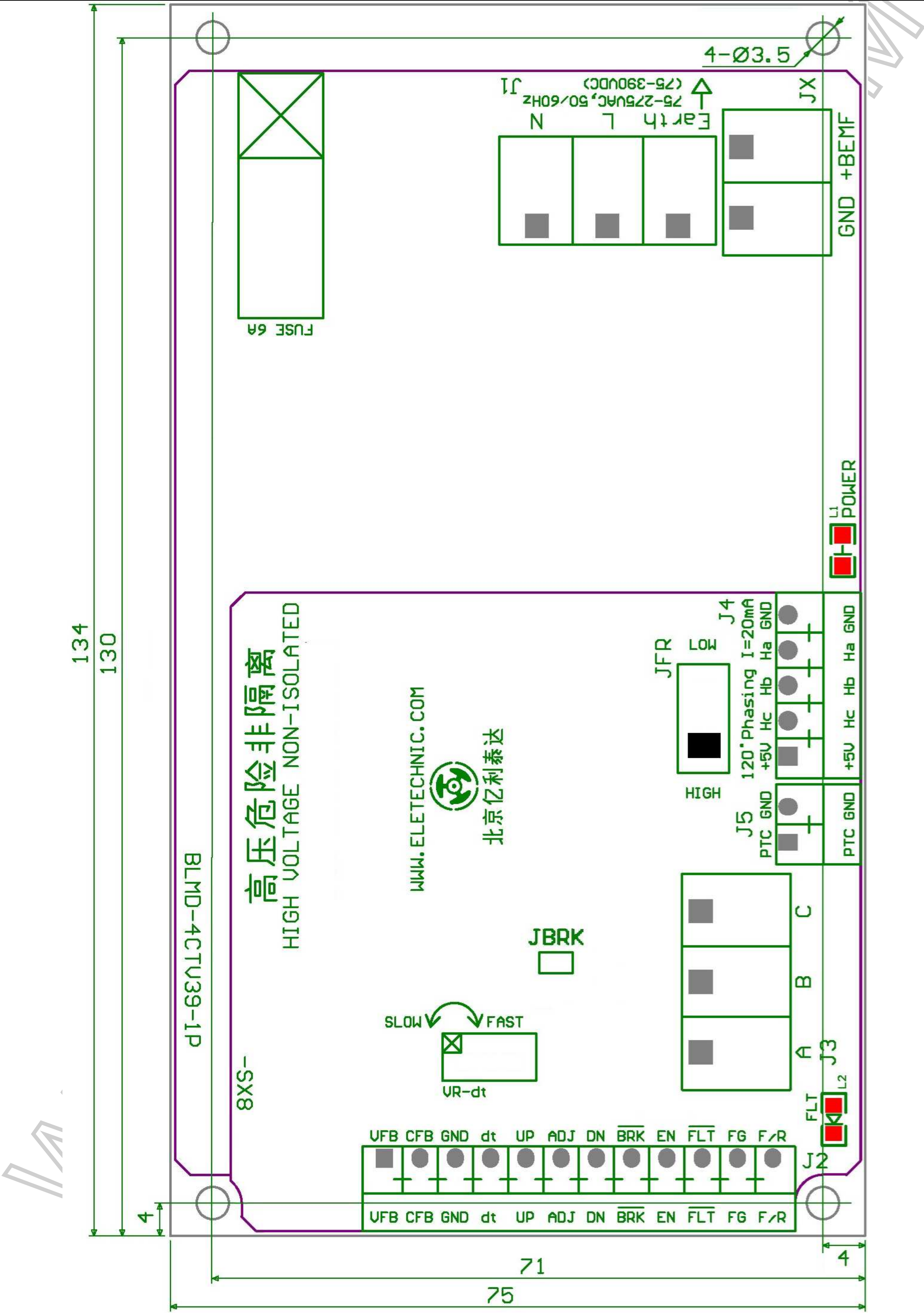
Please find out the JBRK junction, and welded short cut on the PCB board. The brake function is valid.

Driver Dimension and Connection Diagram (Unit: mm)

The driver dimension is 134 (L) * 75 (W) * 65 (H). The approximate weight of the driver is 325g (including intrinsic sink).

The radiator can be custom-made according to the motor power, heating and cooling of the application.

If the surface temperature of the sink is always higher than 85C, cooling fan must be installed. Otherwise the driver would be over temperature lockout.


4-Ø3.5
J1
75-275VAC,50/60Hz
(75-390VDC)
Earth
L
N
JX
+BEMF
GND
FUSE 6A
L1
POWER
J4
120°Phasing I=20mA
+5V
Hc
Hb
Ha
GND
JFR
LOW
HIGH
J5
PTC
GND
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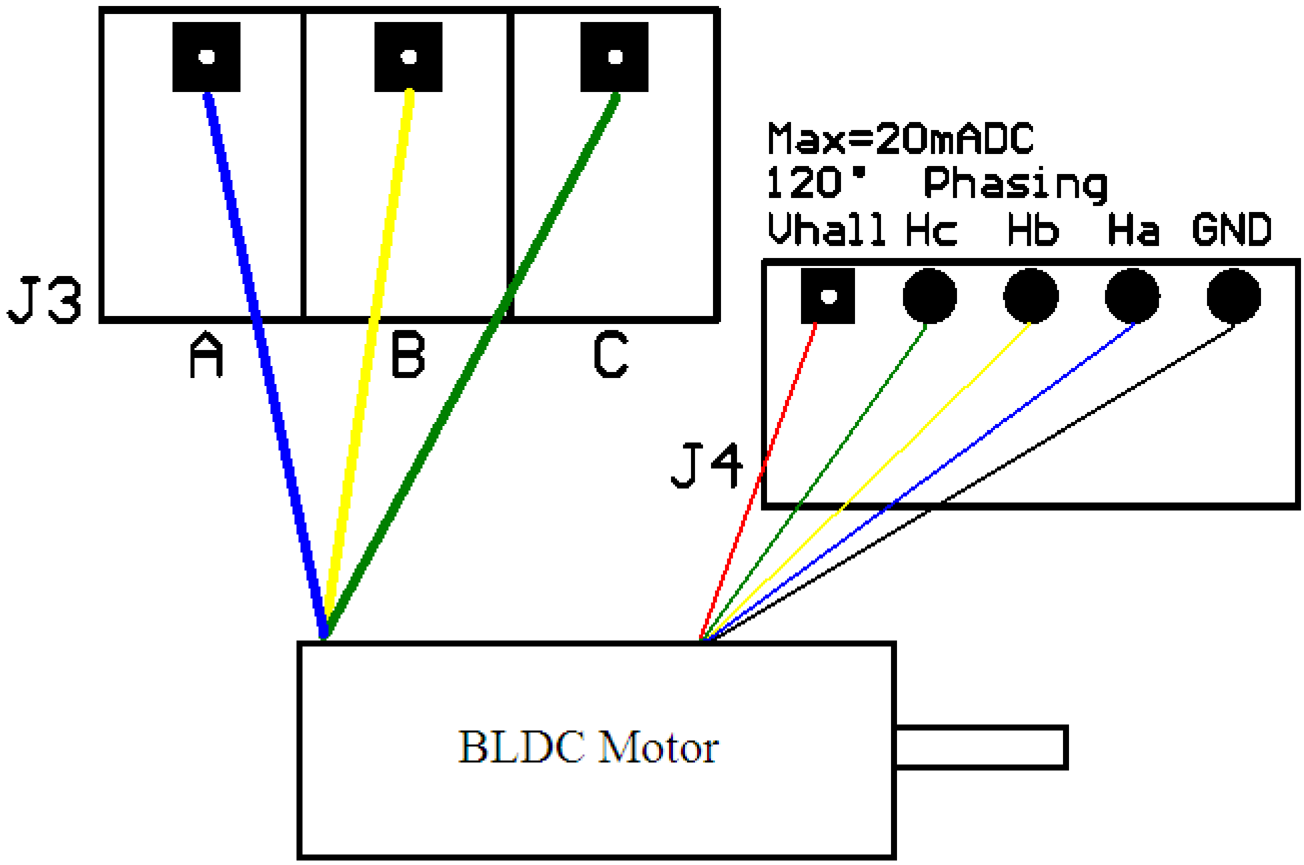
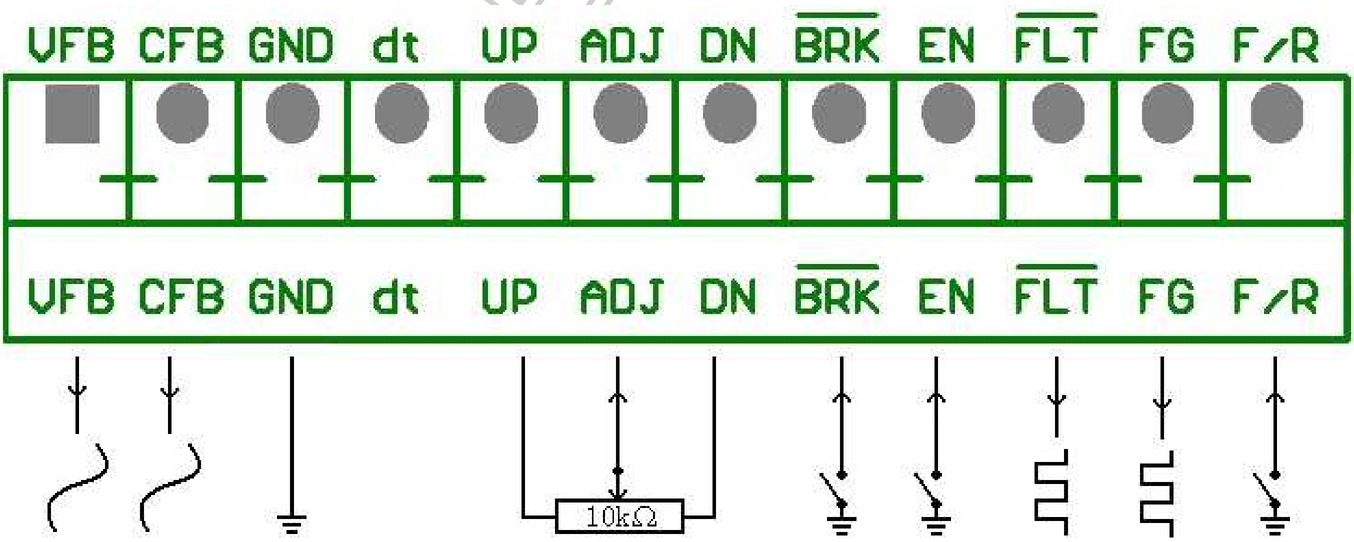
北京亿利泰达
高压危险非隔离
HIGH VOLTAGE NON-ISOLATED
BLMD-4CTV39-1P
8XS-
JBRK
C
B
A
J3
L2
FLT
SLOW
FAST
VR-dt
VFB CFB GND dt UP ADJ DN BRK EN FLT FG F/R
J2
134
130
4
71
75

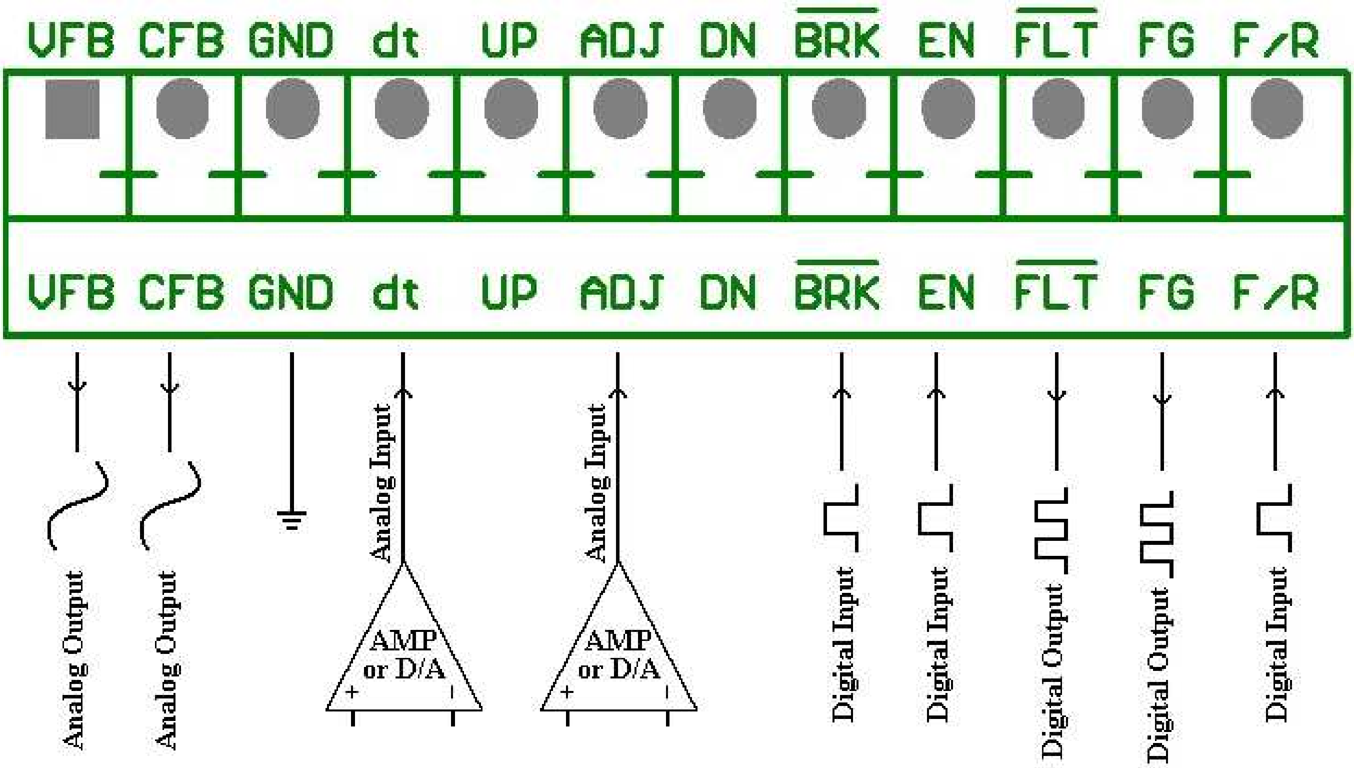
Application Circuit Examples



The Connection of BLDC Motor



The Connection of Mechanical Switches and Potentiometer Speed Control



The Connection of Digital Control and Operational Amplifier (or D/A) Speed Control

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